

II. REMARKS

1. Claims 1-28 remain in the application.
2. Applicants respectfully submit that claims 1, 2, 7-10, and 28 are patentable over the combination of Jönsson et al. (US 5,867,772, hereinafter "Jönsson") in view of Johnson, Jr. et al. (US 6,381,477, hereinafter "Johnson").

Applicants submit that Johnson is not available as a reference against the present application. The present application claims priority under 35 US 119(a) from Finnish application No. 990330, filed February 17, 1999. Johnson has a filing date of November 5, 1999, and therefore is not a valid reference under 35 USC 103(a).

Furthermore, the Office Action correctly points out that Jönsson fails to disclose a reader for a detachable memory module. Jönsson also fails to disclose a frame construction as recited by Applicants' claims 1 and 28. Jönsson discloses a number of components assembled together between a front shell 20 and a base shell 22. However, Jönsson has no disclosure related to a frame construction onto which a printed circuit board, keypad, display, memory module reader, and outer cover parts are mounted.

At least for these reasons, Applicants respectfully submit that the combination of Jönsson and Johnson fails to disclose or suggest all the features of Applicants' claims 1 and 28.

Claims 2 and 7-10 depend from claim 1 and therefore are also patentable over the combination of Jönsson and Johnson.

3. Claims 3-5 and 21 are patentable over the combination of Jönsson in view of Feilner et al. (US 6,463,263, hereinafter "Feilner").

Like Johnson, Feilner is not available as a reference against the present application because Feilner's U.S. filing date of January 28, 2000 does not precede Applicants' priority date of February 17, 1999. Therefore, Feilner is also not a valid reference under 35 USC 103(a).

4. Claim 6 is patentable over the combination of Jönsson in view of Suuronen (US 6,438,393).

Suuronen's U.S. filing date of June 18, 1999 fails to precede Applicants' priority date of February 17, 1999 and is therefore not a valid reference.

5. Applicants respectfully submit that claims 14 and 18 are patentable over the combination of Jönsson in view of Fuhrmann et al. (US Application Publication US 2002/0057792, hereinafter, "Fuhrmann").

As mentioned above, Jönsson fails to disclose a reader for a detachable memory module and also fails to disclose a frame construction as recited by Applicants' claim 1. Fuhrmann also fails to disclose these features. Fuhrmann is silent with respect to a reader for a detachable memory module. Fuhrmann discloses a frame 32 and a retaining frame 35, however, these are used to mount components to the circuit board 23 and do not serve as mounting points for the various components recited by Applicants' claim 1. Therefore, claims 14 and 18 are not obvious over the combination of Jönsson and Fuhrmann.

6. It appears that claims 15-17 and 19 are rejected in view of Davidson et al. (US 6,321,441, hereinafter "Davidson") for obviousness, however the Office Action fails to specifically state under which part of the statute the rejection is being made. Furthermore, it is unclear if Davidson is being combined with other art to supply all the features of the claimed invention. Nevertheless, Davidson's U.S. filing date of October 27, 1999 fails to precede Applicants' priority date of February 17, 1999. Therefore, Davidson cannot be used as a reference against the present application.

7. It also appears that claim 20 is rejected for obviousness in view of Aaltonen et al. (US 6,274,825, hereinafter "Aaltonen"), but again, the Office Action fails to specifically state the part of the statute under which the rejection is made and is unclear as which art, if any, is combined with Aaltonen to reach the rejection. However, Aaltonen's U.S. 102(e) date of September 28, 1999 fails to precede Applicants' priority date of February 17, 1999, and Aaltonen, therefore, is not a valid reference.

8. Claims 22 and 24 are patentable over the combination of Jönsson in view of Takenaka (US 6,456,342), however, Takenaka's U.S. filing date of February 26, 1999 fails to precede the present application's priority date of February 17, 1999 and Takenaka is therefore not a valid reference.

9. Claim 25 is patentable over the combination of Jönsson in view of Beiswanger (US 4,958,911).

As pointed out above, Jönsson fails to disclose a reader for a detachable memory module and fails to disclose a frame construction onto which a printed circuit board, keypad,

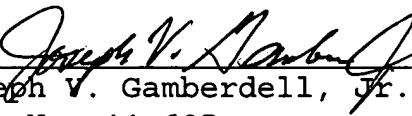
display, memory module reader, and outer cover parts are mounted. Beiswanger has no disclosure related to these features. Therefore, claim 25 is patentable over the combination of Jönsson and Beiswanger.

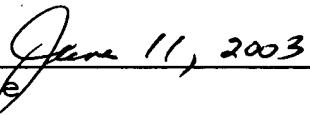
10. It appears that claims 26 and 27 are rejected for obviousness, however, the Office Action fails to specifically state the part of the statute under which the rejection is made and is unclear as which art is applicable. Claims 26 and 27 depend from claim 1 and therefore, for all the reasons argued above in support of claim 1 are patentable over the cited prior art.

For all of the foregoing reasons, it is respectfully submitted that all of the claims now present in the application are clearly novel and patentable over the prior art of record, and are in proper form for allowance. Accordingly, favorable reconsideration and allowance is respectfully requested. Should any unresolved issues remain, the Examiner is invited to call Applicants' attorney at the telephone number indicated below.

The Commissioner is hereby authorized to charge payment for any fees associated with this communication or credit any over payment to Deposit Account No. 16-1350.

Respectfully submitted,

  
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